

In the Specification:

Please amend the paragraph on page 13, lines 17-24, as follows:

--The selected word line WLi is at the word line voltage or reading voltage $Vw1$. All of the unselected word lines or bit lines of the access line devices 6 and 4 are at the equipotential voltage or equalization voltage Veq . Ideally, the equipotential voltage Veq is also present on the selected bit line BLk at its end, namely, at the node 7a and, thus, through line 16, at the potential-sensing connection 22 on the sense-amplifier configuration 20.